

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	9782	processor with control\$3 with (storage or software adj block\$1 or ROM or memory) with (instruction\$1 or command\$1)	USPAT	OR	OFF	2005/04/22 11:08
S2	20	processor with control\$3 with (storage or software adj block\$1 or ROM or memory) with (instruction\$1 or command\$1) same PLL	USPAT	OR	OFF	2005/04/22 11:08
S3	75	processor with control\$3 with (storage or software adj block\$1 or ROM or memory) with (instruction\$1 or command\$1) same oscillator	USPAT	OR	OFF	2005/04/22 11:44
S4	6	processor with control\$3 with (storage or software adj block\$1 or ROM or memory) with (instruction\$1 or command\$1) same oscillator same PLL	USPAT	OR	OFF	2005/04/22 11:08
S5	18	processor with control\$3 with (storage or software adj block\$1 or ROM or memory) with (instruction\$1 or command\$1) same oscillator and PLL	USPAT	OR	OFF	2005/04/22 14:39
S6	66	pll and holdover	USPAT	OR	OFF	2005/04/22 13:50
S7	13	pll same holdover and stratum	USPAT	OR	OFF	2005/04/22 13:51
S8	42	PLL and frequency adj synthesizer and (status adj message or stratum)	USPAT	OR	OFF	2005/04/22 15:26
S9	0	PLL and frequency adj synthesizer and (status adj message or stratum) with (threshold or target adj level or predetermine\$2 adj level)	USPAT	OR	OFF	2005/04/22 15:26
S10	396	PLL and frequency adj synthesizer and (quality adj level or performance or interference or error signal or status adj message or stratum) with (threshold or target adj level or predetermine\$2 adj level)	USPAT	OR	OFF	2005/04/22 15:27
S11	54	PLL and frequency adj synthesizer and (quality adj level or performance or interference or noise adj level or status adj message or stratum) with (threshold or target adj level or predetermine\$2 adj level)	USPAT	OR	OFF	2005/04/22 15:28

S12	0	PLL and frequency adj synthesizer and (quality adj level or performance or interference or noise adj level or status adj message or stratum) with (threshold or target adj level or predetermine\$2 adj level) and holdover	USPAT	OR	OFF	2005/04/22 15:28
S13	8	((("4914404") or ("4933955") or ("5638410") or ("5748569") or ("6078595") or ("6282210") or ("6341149") or ("6570454"))), PLL	USPAT; USOCR	OR	OFF	2005/04/22 15:46

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S1	9	stratum with processor	USPAT	OR	OFF	2005/04/29 14:34
S2	2	stratum with processor and holdover	USPAT	OR	OFF	2005/04/27 11:01
S3	25	(memory or storage or RAM or ROM) with (holdover)	USPAT	OR	OFF	2005/04/27 11:02
S4	6	(memory or storage or RAM or ROM) with (holdover) and processor	USPAT	OR	OFF	2005/04/27 11:06
S5	3	(memory or storage or RAM or ROM) with (holdover) same PLL	USPAT	OR	OFF	2005/04/27 11:27
S6	3	(processor) with (holdover) with PLL	USPAT	OR	OFF	2005/04/27 16:57
S7	5	(processor) with (holdover) with PLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:43
S8	6	(processor) with PLL same holdover	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 14:27
S9	7	((("3573649") or ("3903482") or ("5220684") or ("5559686") or ("6072842") or ("6078224") or ("6150888")).PN.	USPAT; USOCR	OR	OFF	2005/04/27 16:57
S10	7	(processor) with (holdover) and PLL	USPAT	OR	OFF	2005/04/27 17:01
S11	4	(processor) with PLL same holdover	USPAT	OR	OFF	2005/04/27 17:02
S12	11	((("5317601") or ("5990715") or ("5994934") or ("6049239") or ("6242954") or ("6313676") or ("6323705") or ("6384649") or ("6424185") or ("6441692") or ("6462623")).PN.	USPAT; USOCR	OR	OFF	2005/04/28 10:07
S13	12	processor with holdover	USPAT	OR	OFF	2005/04/28 10:07
S14	7	processor with holdover and pll	USPAT	OR	OFF	2005/04/28 11:11
S15	7	processor same holdover and pll and (performance or status or quality adj level)	USPAT	OR	OFF	2005/04/28 11:12
S16	5	processor same holdover and pll and (performance or status or quality adj level) with signal	USPAT	OR	OFF	2005/04/28 11:42
S25	93	quality with clock adj signal and pll and (lock\$2 or holdover)	USPAT	OR	OFF	2005/04/28 13:40

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S27	8	quality with clock adj signal with process\$3 and pll with (lock\$2 or holdover)	USPAT	OR	OFF	2005/04/28 13:47
S30	4	quality with clock adj signal with processor and pll with (lock\$2 or holdover)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 13:52
S31	6	quality with clock adj signal same processor and pll with (lock\$2 or holdover)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 13:54
S32	6	quality with clock adj signal same processor and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 13:54
S34	13	quality with clock adj signal and pll with (hold\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 14:11
S35	16	quality with clock adj signal and holdover	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 14:37
S36	1	quality with clock adj signal with processor and holdover	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 14:13
S37	1	quality with clock adj signal same processor and holdover	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 14:13
S43	716	pll with (hold\$4 or unlock\$3 or lock\$3) and (quality or status or performance) with (clock adj signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 15:28

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S45	9	(375/371-376).CCLS. and ((phase adj lock\$4 adj loop\$3) or (PLL)) with (unlock\$3) with (level or threshold or limit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/29 18:31
S46	6	(375/371-376).CCLS. and ((phase adj lock\$4 adj loop\$3) or (PLL)) with (unlock\$3) with (level or threshold or limit) with (monitor or watch or check or detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 13:18
S47	8	(375/371-376).CCLS. and ((phase adj lock\$4 adj loop\$3) or (PLL)) with (unlock\$3) same (level or threshold or limit) with (monitor or watch or check or detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 13:19
S48	17	(375/371-376).CCLS. and ((phase adj lock\$4 adj loop\$3) or (PLL)) with (unlock\$3 or hold\$4) same (level or threshold or limit) with (monitor or watch or check or detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/02 17:28
S49	2	(375/371-376).CCLS. and ((phase adj lock\$4 adj loop\$3) or (PLL)) with (unlock\$3 or hold\$4) same (level or threshold or limit) and frame adj pulse	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 18:12
S50	3	receive with data and frame adj pulse with recovered with clock adj signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 18:15
S51	590	receive with data and frame adj pulse	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 18:15
S52	68	receive with data and frame adj pulse and PLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 18:16

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S54	1	receive with data and frame adj pulse and ((phase adj lock\$4 adj loop\$3) or (PLL)) with (unlock\$3 or hold\$4) same (level or threshold or limit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 18:17
S55	12	receive with data and frame adj pulse and ((phase adj lock\$4 adj loop\$3) or (PLL)) with (unlock\$3 or hold\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/01 18:17
S56	13	(first) with ((phase adj lock\$3 adj loop\$3) or (PLL)) and (frame adj pulse) and (second) adj ((phase adj lock\$3 adj loop) or (PLL))	USPAT	OR	OFF	2005/05/02 13:12
S58	21	((phase adj lock\$4 adj loop\$3) or (PLL)) and shelf adj backplane	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/02 17:28
S59	5	((phase adj lock\$4 adj loop\$3) or (PLL)) and shelf adj backplane and shelf adj controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/02 17:28
S62	2	(threshold or level or limit) with quality with ((phase adj lock\$3 adj loop) or (PLL)) with (unlock\$3 or hold\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 09:37
S63	5357	stratum with ("2" or two)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 09:37
S64	62	stratum with ("2" or two) and ((phase adj lock\$3 adj loop) or (PLL))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 09:37
S65	10	stratum with ("2" or two) and ((phase adj lock\$3 adj loop) or (PLL)) with (hold\$4 or unlock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 11:26

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S66	21	((phase adj) lock\$3 adj loop) or (PLL)) and shelf adj backplane	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 11:27
S67	4	((phase adj) lock\$3 adj loop) or (PLL)) and shelf adj backplane with synchroniz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 11:44
S68	3	((phase adj) lock\$3 adj loop) or (PLL)) and shelf adj backplane same synchroniz\$5 with shelf adj element\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 11:45
S69	4	((phase adj) lock\$3 adj loop) or (PLL)) and shelf adj backplane same synchroniz\$5 with shelf	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/03 11:45

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	569	(holdover or unlock\$4 or locking) same (computer with processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 15:54
L3	29	(holdover or unlock\$4 or locking) same (computer with processor) and ((phase adj) lock\$3 adj) loop) or (PLL))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 15:55
L4	5	(holdover or unlock\$4 or locking) same (computer with processor) same ((phase adj) lock\$3 adj) loop) or (PLL))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 15:56
L6	2	((phase adj) lock\$3 adj) loop) or (PLL)) with (control\$3) same (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 15:57
L7	6	((phase adj) lock\$3 adj) loop) or (PLL)) with (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 16:14
L8	4	(phase adj) lock\$3) with (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 16:51
S1	6	((PLL) or (phase adj) lock\$3 adj) loop\$3)) with (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 17:51
S2	261	((PLL) or (phase adj) lock\$3 adj) loop\$3)) and (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 17:51

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S4	3	((PLL) or (phase adj) lock\$3 adj) loop\$3)) same (holdover or unlock\$4) and (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 09:42
S6	23	((PLL) or (phase adj) lock\$3 adj) loop\$3)) same (hold\$4 or unlock\$4) and (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:26
S7	187	((PLL) or (phase adj) lock\$3 adj) loop\$3)) same (hold\$4 or unlock\$4 or locked) and (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:27
S8	4	((PLL) or (phase adj) lock\$3 adj) loop\$3)) and (hold\$4 or unlock\$4) with (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:26
S9	182	((PLL) or (phase adj) lock\$3 adj) loop\$3)) with (hold\$4 or unlock\$4 or locked) and (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:27
S10	7105	((PLL) or (phase adj) lock\$3 adj) loop\$3)) same (hold\$4 or unlock\$4) or (PLL with (locked or locking)) and (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:28
S12	84	((PLL) with (locked or locking)) and (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:29
S13	1	((PLL) with (locked or locking)) same (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:29

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S14	1	((PLL) with (locked or locking or hold\$3 or unlock\$3)) same (computer adj) processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/10 19:29
S16	5	((PLL) or (phase adj) lock\$3 adj) loop\$3)) same (holdover or unlock\$4 or locking) same (computer with processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 09:48
S17	230	((PLL) or (phase adj) lock\$3 adj) loop\$3)) same (holdover or unlock\$4 or locking) and (computer with processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 09:49
S18	3	((PLL) or (phase adj) lock\$3 adj) loop\$3)) same (holdover or unlock\$4 or locking) and (computer adj) processor) with ((PLL) or (phase adj) lock\$3 adj) loop\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 10:22
S19	6	(computer adj) processor) with ((PLL) or (phase adj) lock\$3 adj) loop\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 10:50
S20	2	(computer adj) processor) with (holdover or unlock\$3 or lock\$3) and PLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 11:39
S21	1520	(processor) same (holdover or unlock\$3 or lock\$3) with PLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 11:45
S22	677	(processor) with (holdover or unlock\$3 or lock\$3) with PLL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 11:45

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S23	21	(processor) with (holdover or unlock\$3 or lock\$3) with PLL and machine adj1 readable adj medium	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 11:52
S25	3	(processor) with (holdover or unlock\$3 or locking) with ((PLL) or (phase adj) lock\$3 adj) loop) and machine adj1 readable adj medium	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 12:25
S26	53	(processor) with (holdover or unlock\$3 or locking) with ((PLL) or (phase adj) lock\$3 adj) loop))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/11 11:53

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